**WEEKLY PROGRESS REPORT**

**Sightline Capstone Project**

**Week 11**

**23 March 2019**

**Tai:**

**This week:**

**Next week:**

**Question:**

**Kimball:**

**This Week:**

* Created SLA1500 CAM layout and schematic REV 4.0
* Defined Layers and vias for SLA1500 CAM layout
* Designed power layer for SLA1500 CAM
* Working on Top and Bottom layer component placement for layout

**Next Week:**

* Generate new BOM for SLA1500 CAM
* Finish component placement for layout
* Route all traces
* Finalize layout for first review

**Comments/Questions:**

Regarding [SLA1500 CAM REV 4.0 schematic](https://github.com/phamtaiece/Capstone-Sightline/blob/master/EAGLE%20files/Images/SLA1500_CAM_V4.pdf)

* Originally for the discrete GPIO connection J4 P4 was assigned to CAM\_GPIO175. GPIO175 was changed to control standby on the AR0134. I changed J4 P4 to CAM\_GPIO178 which had no connection.
* Should U1 P23 OE\_BAR be tied to ground or does Sightline want to control this?
* Are J1 P11 and P12 CAM\_FSYNC and CAM\_LSYNC no connects?
* J2 P4 and P5 no longer have a connection. Should these be tied to ground with zero ohm resistors?